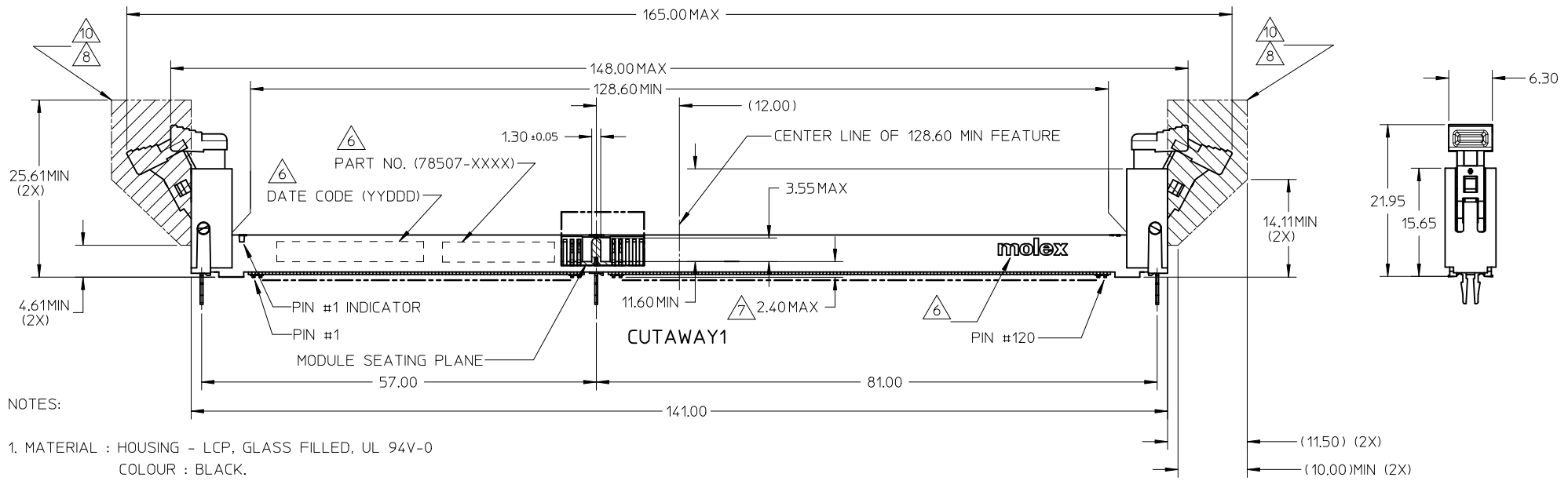
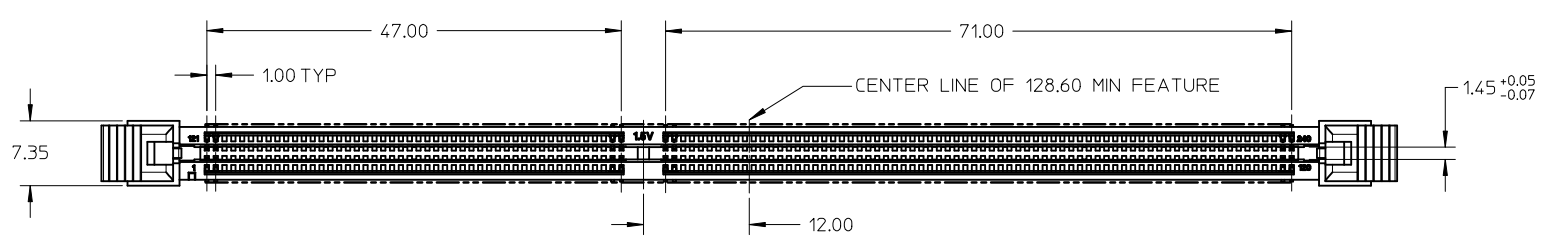


10 9 8 7 6 5 4 3 2 1

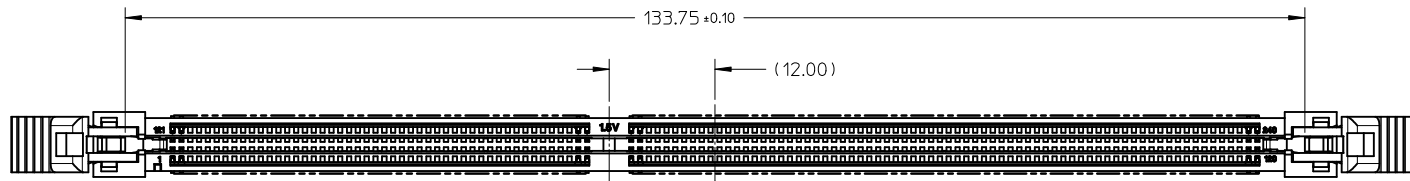


NOTES:

1. MATERIAL : HOUSING - LCP, GLASS FILLED, UL 94V-0  
COLOUR : BLACK.  
LATCH - NYLON, GLASS FILLED, UL 94V-0  
COLOUR : SEE TABLE ON SHEET 10.  
TERMINAL : COPPER ALLOY.
2. FINISHES - SEE TABLE ON SHEET 10.
3. PRODUCT SPECIFICATION : PS-78507-001
4. PACKAGING SPECIFICATION: PRODUCT SHALL BE PACKED IN TRAY, SEE SHEET 10.
5. CARD SLOT ACCEPTS 1.27±0.10MM MODULE THICKNESS (MEASURED FROM P.C. PADS).
6. MOLEX LOGO, DATE & PART NO. INDICATED ON HOUSING.
7. MODULE SEATING PLANE FROM TOP OF PCB.
8. KEEP OUT ZONE RESERVED FOR LATCH.
9. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR (INCLUDED LATCHES IN OPENING POSITION) IS MOUNTED ON THE PCB.
10. KEEP OUT ZONE AS PER JEDEC SO-014.

UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	DESCRIPTION REV	QUALITY SYMBOLS		GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE		SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	
						MM ONLY		NTS	METRIC		
						DRAWN BY DATE		TITLE			
						CHECKED BY DATE					
				APPROVED BY DATE							
				MATERIAL NO.		SEE TABLE		DOCUMENT NO.		SHEET NO.	
				DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			

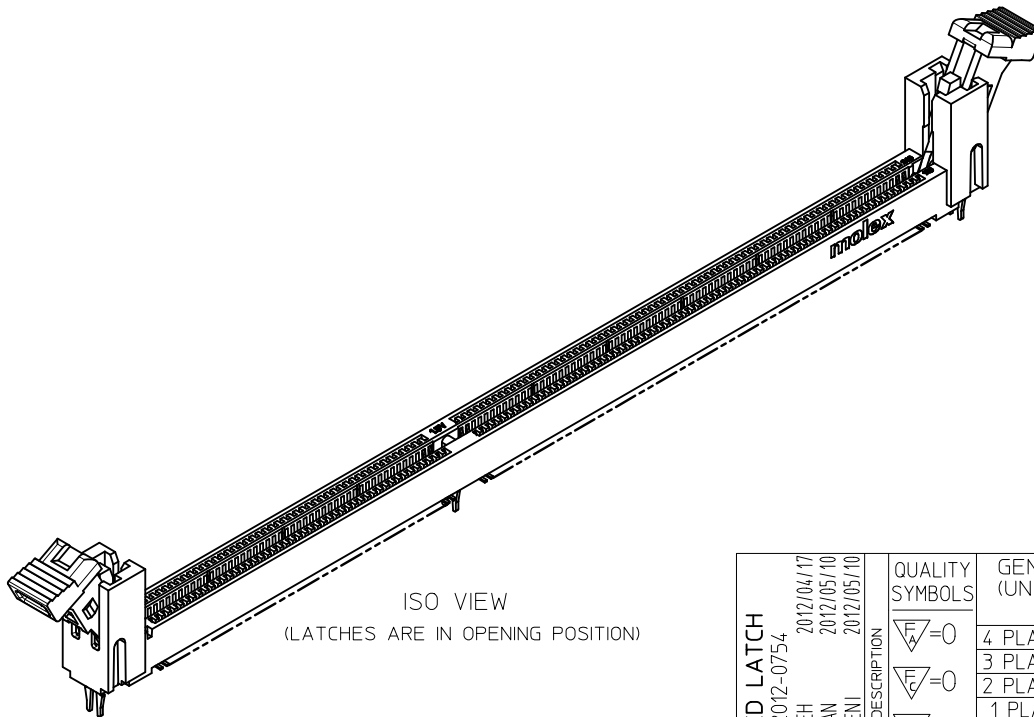
9 8 7 6 5 4 3 2 1



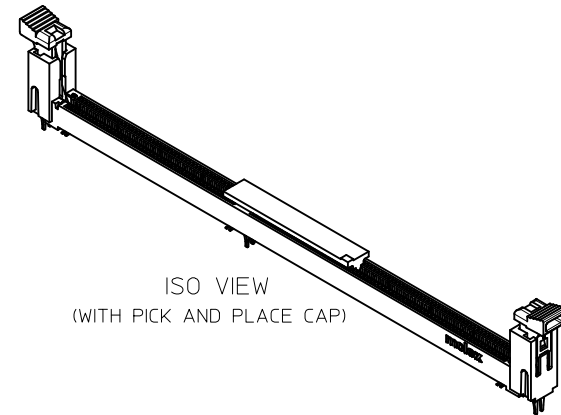
TOP VIEW  
(LATCHES ARE IN OPENING POSITION)



BOTTOM VIEW  
(LATCHES ARE IN OPENING POSITION)



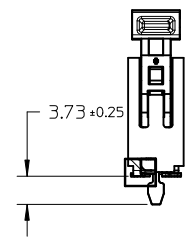
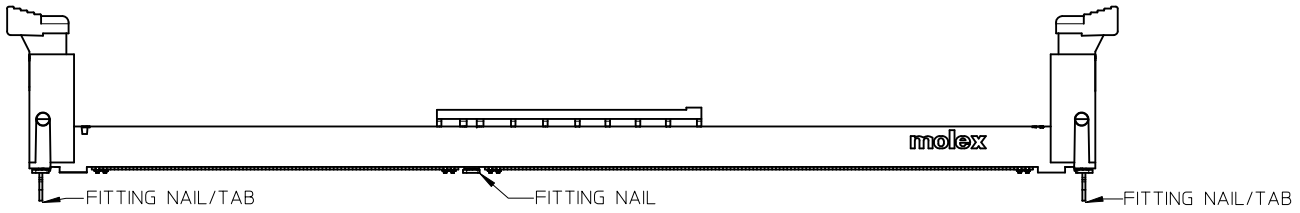
ISO VIEW  
(LATCHES ARE IN OPENING POSITION)



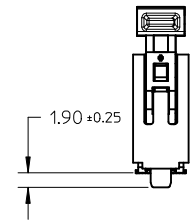
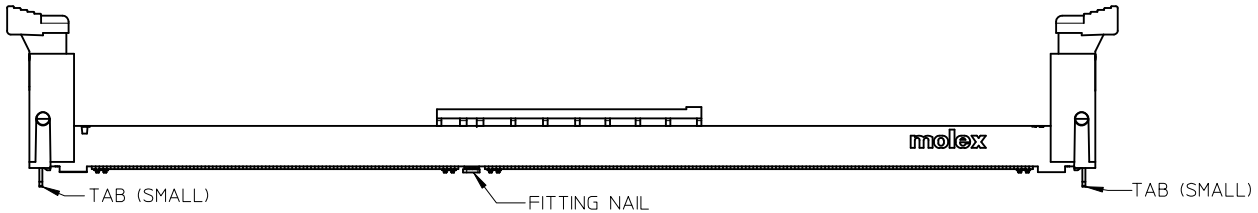
ISO VIEW  
(WITH PICK AND PLACE CAP)

UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	QUALITY SYMBOLS $F_A=0$ $F_C=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE <b>MM ONLY</b>	SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
			mm	INCH	DRAWN BY JAKEEMEW	DATE 2009/04/22	TITLE DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR	
		4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2009/05/27	MOLEX INCORPORATED	
		3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2010/12/15	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-78507-002
			2 PLACES ± 0.20 1 PLACE ± ---	ANGULAR ± 3 °	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SHEET NO. 2 OF 10	
9	REV	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

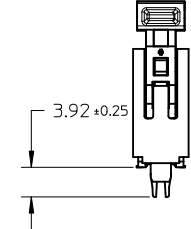
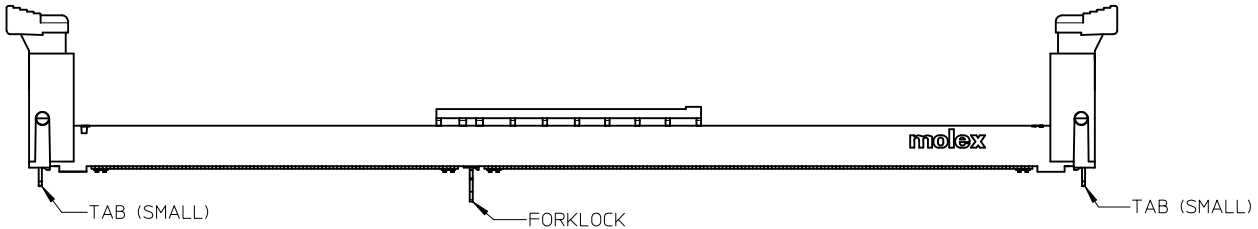
VIEW FOR 78507 FITTING NAIL/TAB (WITH PNP CAP) VERSION (TYPE B)



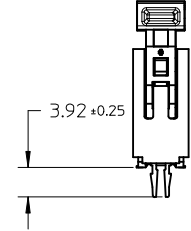
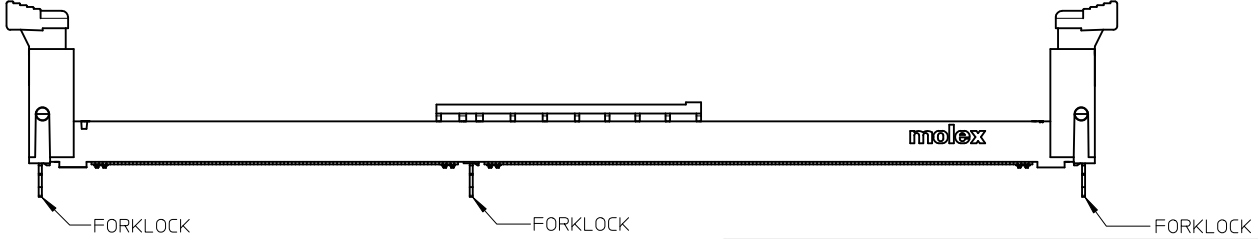
VIEW FOR 78507 TAB (SMALL) (WITH PNP CAP) VERSION (TYPE C)



VIEW FOR 78507 FORKLOCK & TAB (SMALL) (WITH PNP CAP) VERSION (TYPE D)

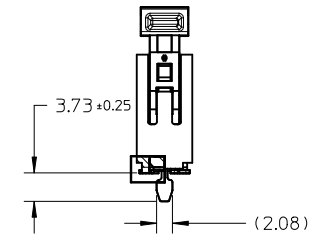
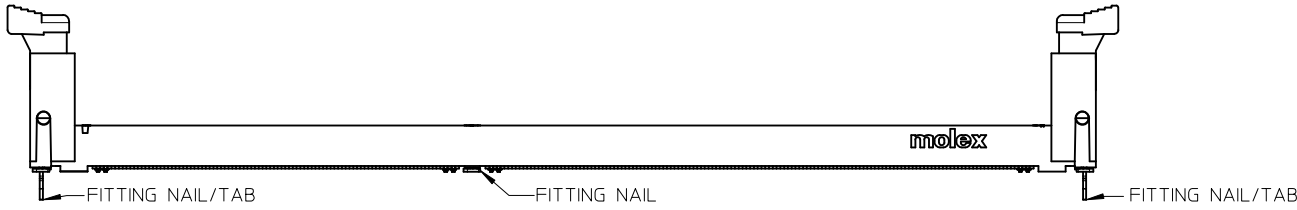


VIEW FOR 78507 FORKLOCK (WITH PNP CAP) VERSION (TYPE E)

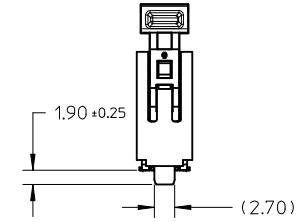
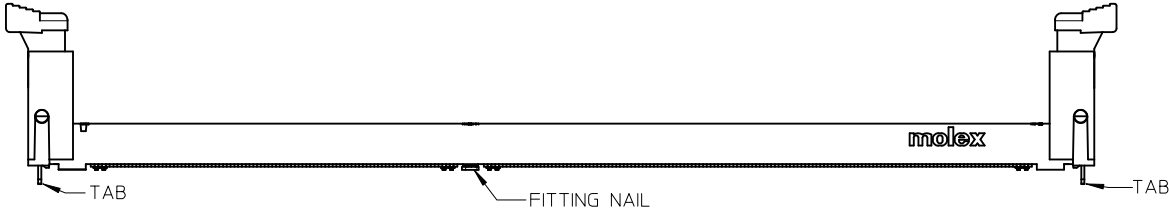


UPDATED LATCH EC NO: S2012-0754 DRWN: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	DESCRIPTION QUALITY SYMBOLS $F_A=0$ $F_C=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY JAKEEMEW	DATE 2009/04/22	TITLE DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR			
		4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2009/05/27	MOLEX INCORPORATED			
		3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2010/12/15	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-78507-002	SHEET NO. 3 OF 10	
2 PLACES	± 0.20	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					
1 PLACE	± ---	± ---	ANGULAR ± 3 °							

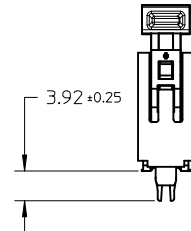
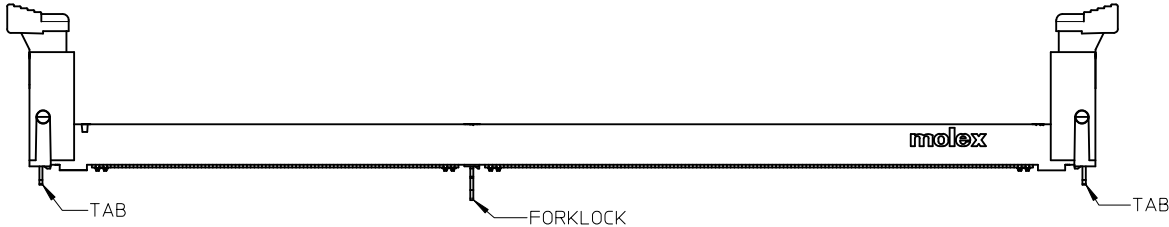
VIEW FOR 78507 FITTING NAIL/TAB (WITHOUT PNP CAP) VERSION (TYPE B)



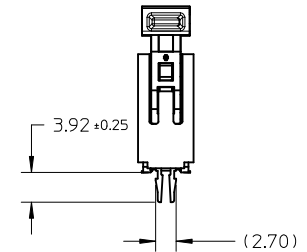
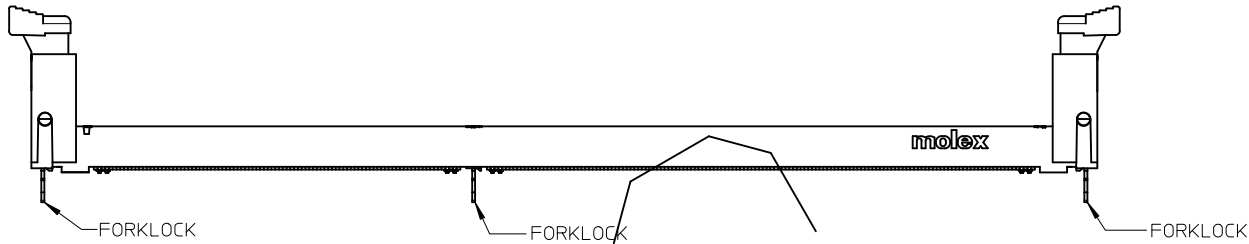
VIEW FOR 78507 TAB (SMALL) (WITHOUT PNP CAP) VERSION (TYPE C)



VIEW FOR 78507 FORKLOCK & TAB (SMALL) (WITHOUT PNP CAP) VERSION (TYPE D)



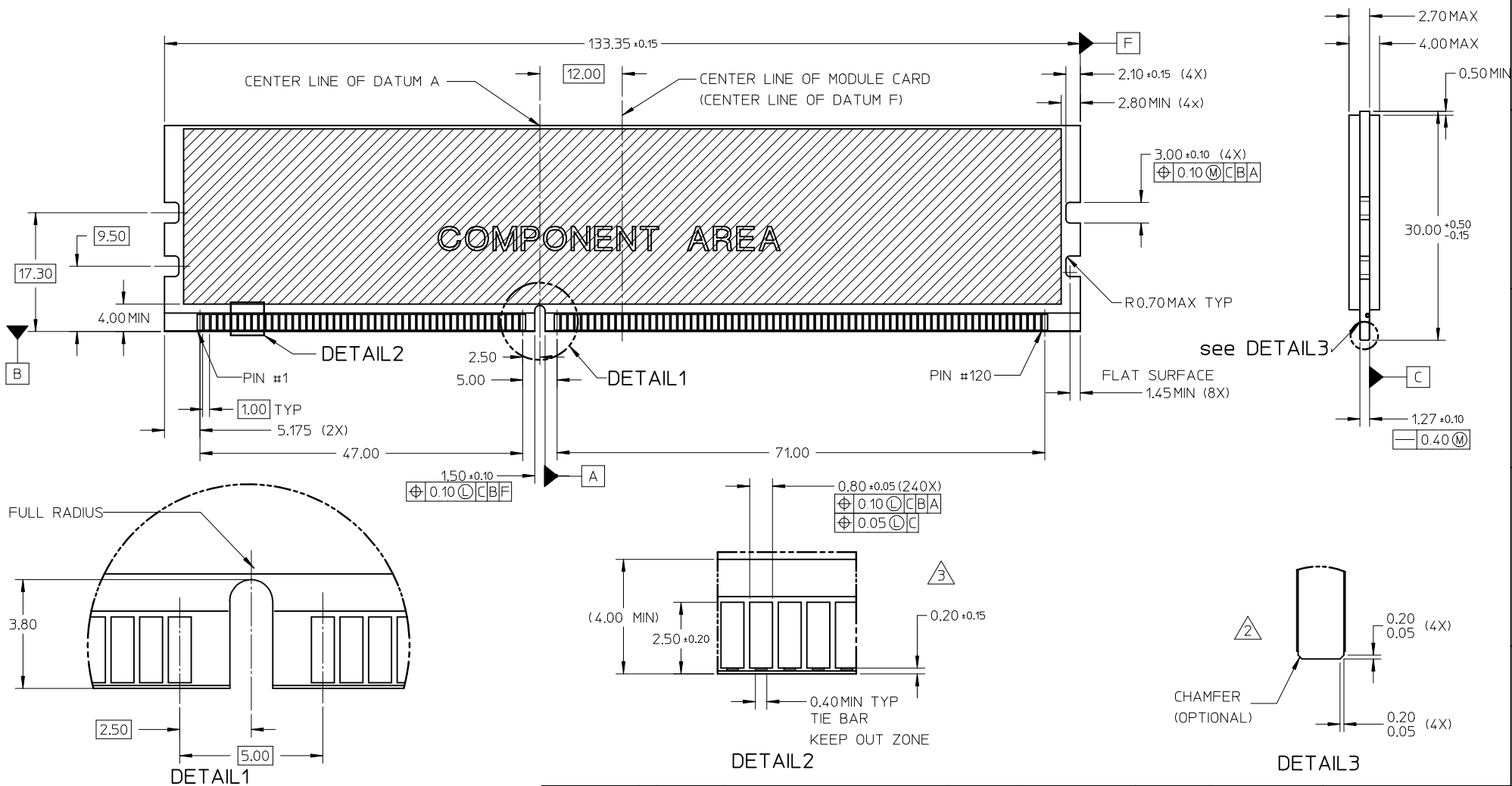
VIEW FOR 78507 FORKLOCK (WITHOUT PNP CAP) VERSION (TYPE E)



UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	DESCRIPTION REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION
		$\nabla_A = 0$ $\nabla_C = 0$ $\nabla_P = 0$	mm    INCH 4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± --- ± --- ANGULAR ± 3 °	DRAWN BY JAKEEMEW DATE 2009/04/22	TITLE		DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR MOLEX INCORPORATED		
			CHECKED BY CCTEH DATE 2009/05/27	MOLEX INCORPORATED					
			APPROVED BY SHLENI DATE 2010/12/15	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-78507-002	SHEET NO. 4 OF 10			
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

10 9 8 7 6 5 4 3 2 1

MODULE CARD  
 DDR3 DUAL IN-LINE MEMORY MODULE FAMILY 1.00MM CONTACT CENTERS  
 (JEDEC MO-269, ISSUE B, APRIL/06)



NOTES:

- RECOMMENDED PLATING FOR CONTACT PADS:  
 PREFERRED: 0.76 MICROMETERS GOLD PLATING MIN OVER 2.00 MICROMETERS NICKEL.  
 ALTERNATIVE: 0.05-0.75 MICROMETERS GOLD PLATING OVER 2.00 MICROMETERS NICKEL MUST USE AN ELECTRONIC CONTACT GRADE CORROSIVE BARRIER LUBRICANT.
- CHAMFER IS OPTIONAL AND IS NOT TO HIT THE GOLD PADS.
- LEADING EDGE OF GOLD PADS SPECIFIED BY KEEPOUT ZONE SHALL BE FREE OF BURRS AND EXTERNAL TIE BARS.

UPDATED LATCH EC NO: S2012-0754 DRWN: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	DESCRIPTION QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
			mm	INCH	DRAWN BY JAKEEMEW	DATE 2009/04/22	TITLE DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR				
			4 PLACES	± ---	± ---	CHECKED BY CCTEH	DATE 2009/05/27	MOLEX INCORPORATED DOCUMENT NO. SD-78507-002 SHEET NO. 5 OF 10			
			3 PLACES	± ---	± ---	APPROVED BY SHLENI	DATE 2010/12/15				
	2 PLACES	± 0.20	± ---	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SEE TABLE		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
	1 PLACE	± ---	± ---	ANGULAR ± 3°		SIZE A3					

9 8 7 6 5 4 3 2 1

10 9 8 7 6 5 4 3 2 1

F

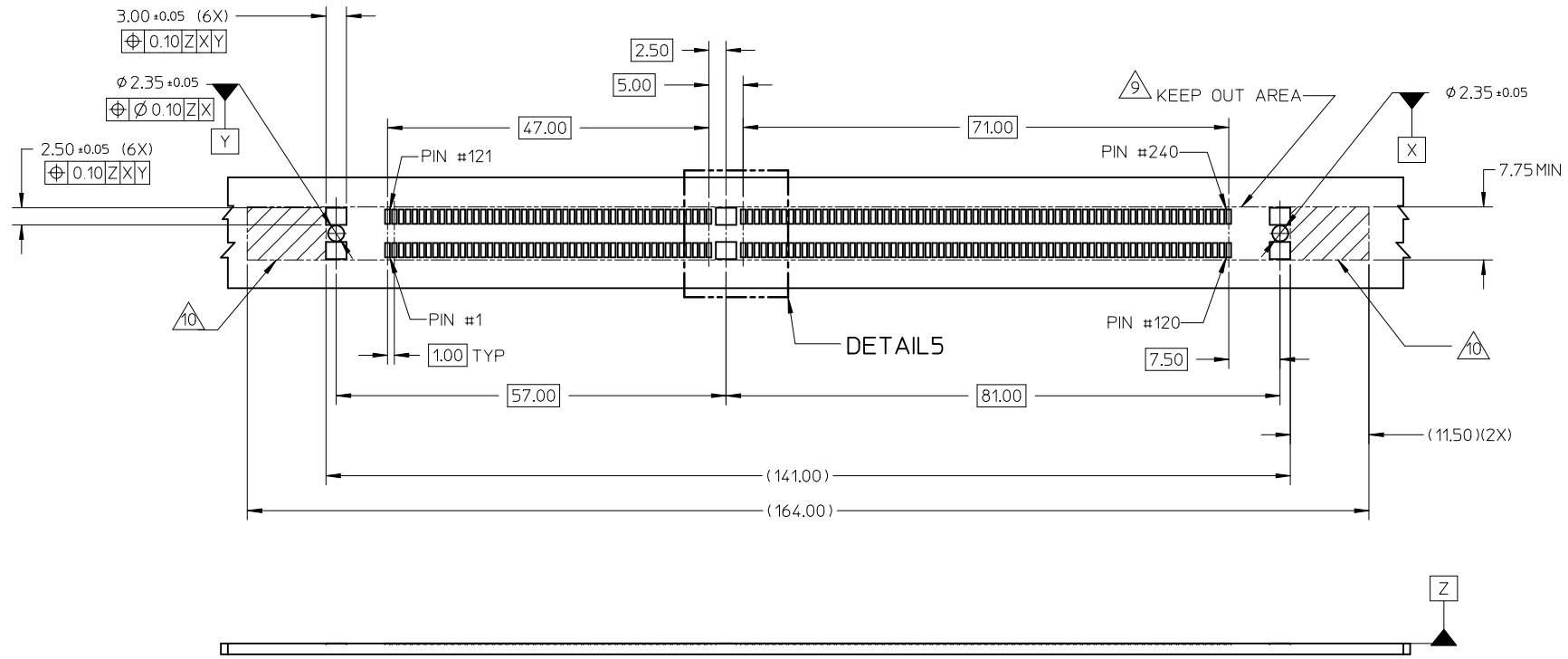
E

D

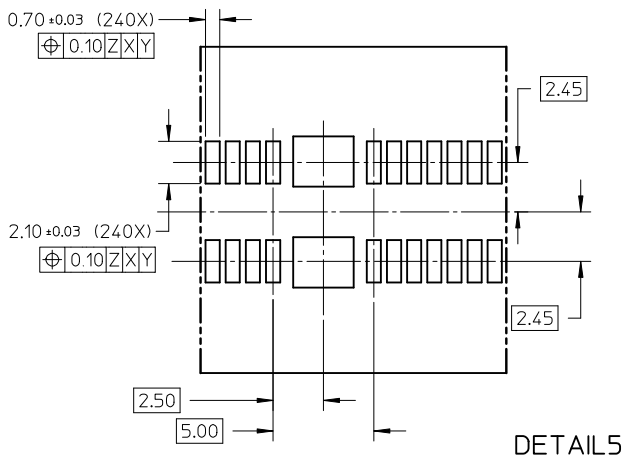
C

B

A



RECOMMENDED PCB THICKNESS = 1.57mm ~ 3.18mm  
 RECOMMENDED STENCIL THICKNESS = 150 MICROMETERS



TYPE B

UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	QUALITY SYMBOLS $F_A=0$ $F_C=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
			mm	INCH	DRAWN BY JAKEEMEW	DATE 2009/04/22	TITLE DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR		
		4 PLACES 3 PLACES 2 PLACES 1 PLACE	$\pm$ --- $\pm$ --- $\pm 0.20$ $\pm$ ---	$\pm$ --- $\pm$ --- $\pm$ --- $\pm$ ---	CHECKED BY CCTEH	DATE 2009/05/27	MOLEX INCORPORATED		
		ANGULAR $\pm 3^\circ$		APPROVED BY SHLENI		DATE 2010/12/15	MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-78507-002	SHEET NO. 6 OF 10
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				SIZE A3	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

9 8 7 6 5 4 3 2 1

10 9 8 7 6 5 4 3 2 1

F

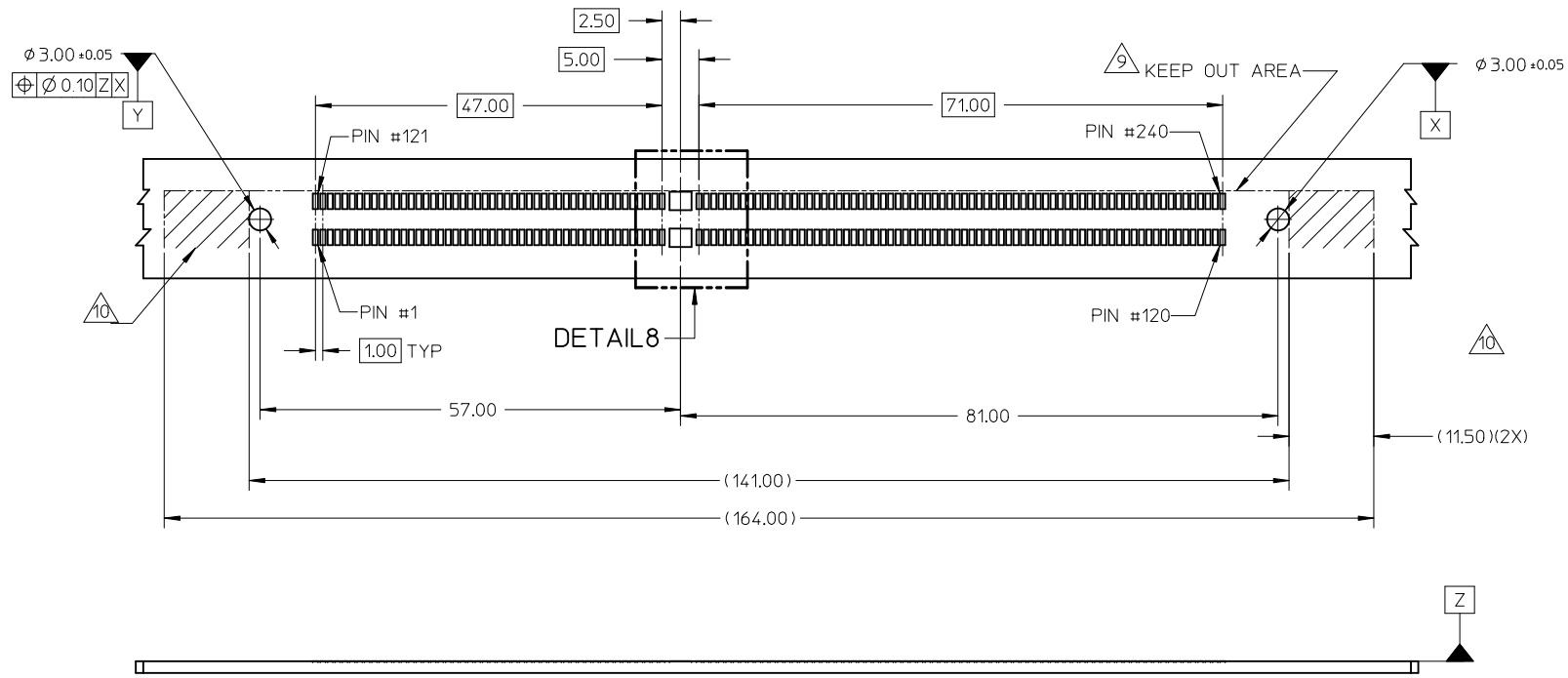
E

D

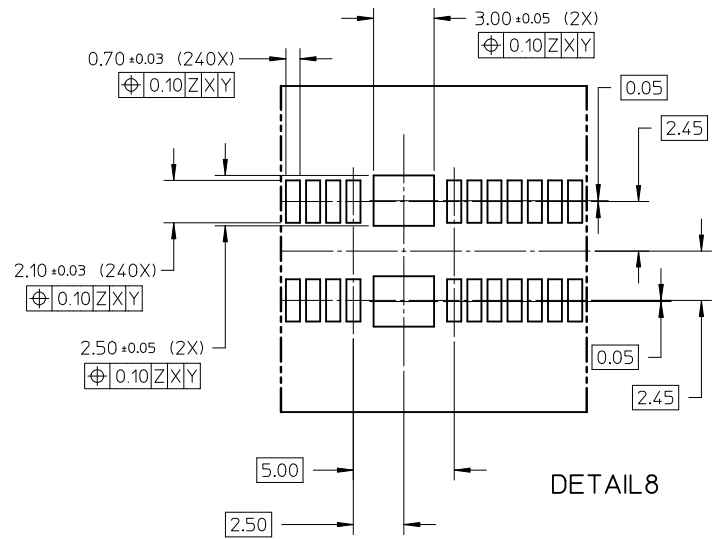
C

B

A



RECOMMENDED PCB THICKNESS = 1.57mm ~ 3.18mm  
 RECOMMENDED STENCIL THICKNESS = 150 MICROMETERS

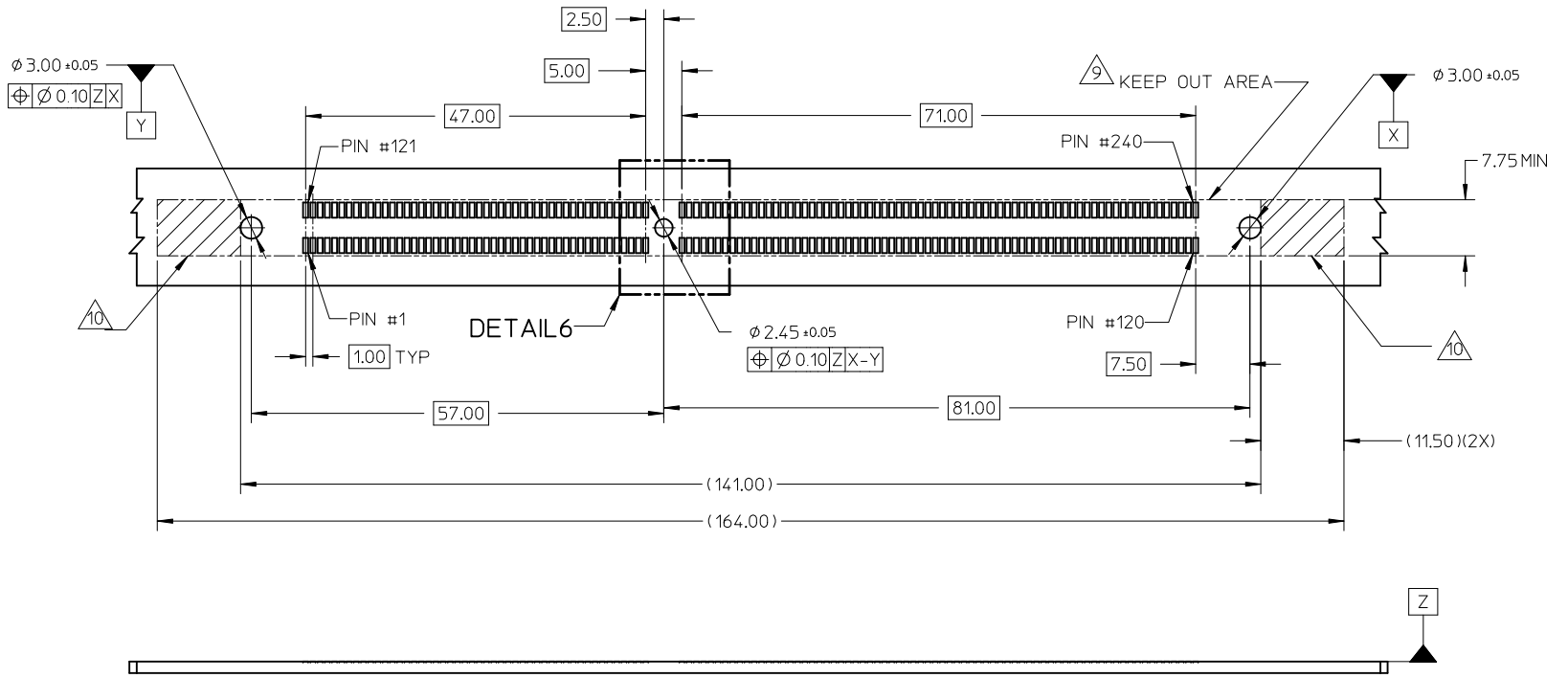


TYPE C

UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH CHKD: CGTAN APPR: SHLENI 2012/04/17 2012/05/10 2012/05/10	QUALITY SYMBOLS $F_A=0$ $F_C=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED) mm INCH 4 PLACES $\pm$ --- $\pm$ --- 3 PLACES $\pm$ --- $\pm$ --- 2 PLACES $\pm 0.20$ $\pm$ --- 1 PLACE $\pm$ --- $\pm$ --- ANGULAR $\pm 3^\circ$	DIMENSION STYLE MM ONLY DRAWN BY DATE JAKEEMEW 2009/04/22 CHECKED BY DATE CCTEH 2009/05/27 APPROVED BY DATE SHLENI 2010/12/15	SCALE NTS DESIGN UNITS METRIC THIRD ANGLE PROJECTION	TITLE DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR MOLEX INCORPORATED DOCUMENT NO. SD-78507-002	SHEET NO. 7 OF 10
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	MATERIAL NO. SEE TABLE	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
	SIZE A3					
	REV 9					

9 8 7 6 5 4 3 2 1

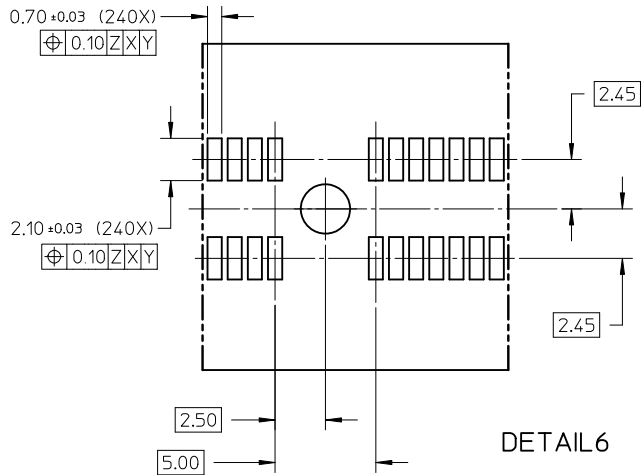
10 9 8 7 6 5 4 3 2 1



RECOMMENDED PCB THICKNESS = 1.57mm ~ 3.18mm

RECOMMENDED STENCIL THICKNESS = 150 MICROMETERS

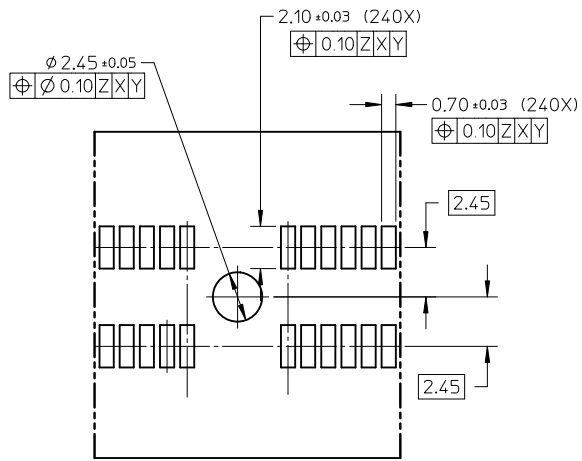
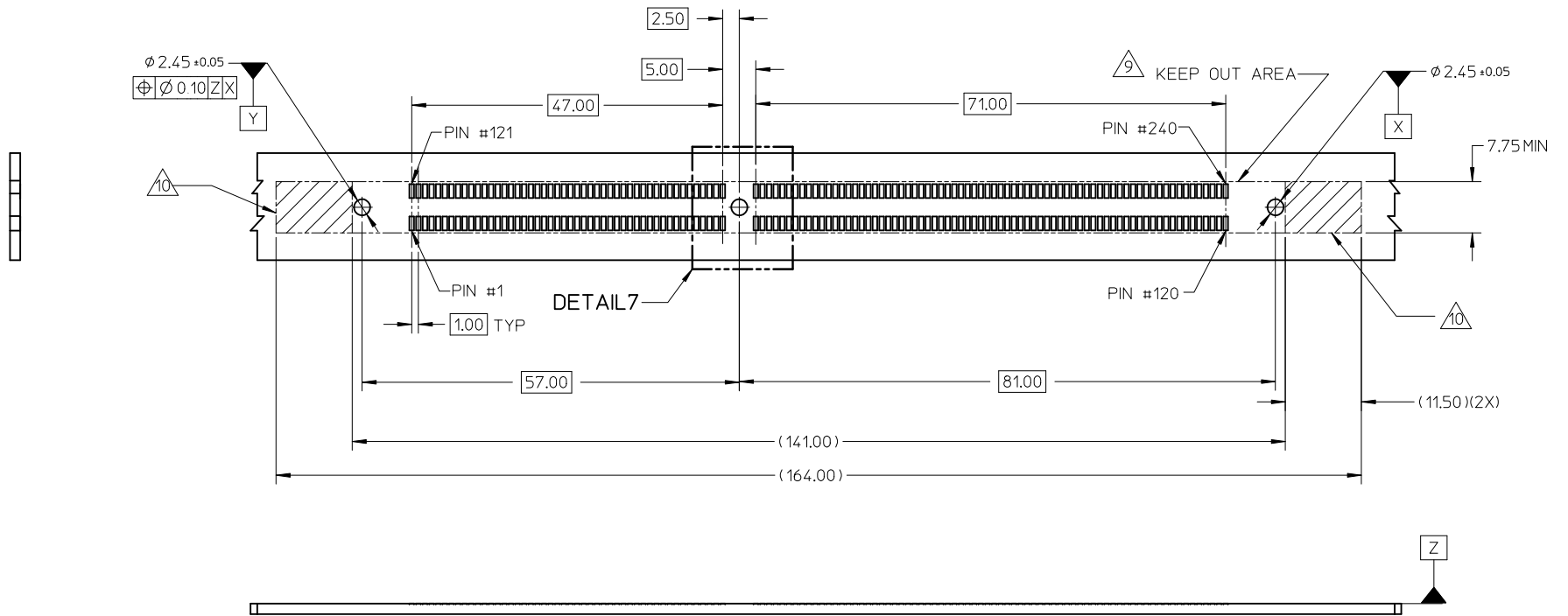
TYPE D



UPDATED LATCH EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	QUALITY SYMBOLS $F_{A0}$ $F_{C0}$ $F_{P0}$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	TITLE			
		4 PLACES	± ---	± ---	JAKEEMEW	2009/04/22	DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR			
		3 PLACES	± ---	± ---	CCTEH	2009/05/27				
	ANGULAR ± 3 °		APPROVED BY	DATE	MOLEX INCORPORATED					
2 PLACES	± 0.20	± ---	SHLENI	2010/12/15						
1 PLACE	± ---	± ---	MATERIAL NO.		DOCUMENT NO.					
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			SEE TABLE		SD-78507-002				SHEET NO.	
			SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				8 OF 10	

9 8 7 6 5 4 3 2 1





DETAIL7

RECOMMENDED PCB THICKNESS = 1.57mm ~ 3.18mm  
 RECOMMENDED STENCIL THICKNESS = 150 MICROMETERS

TYPE E

UPDATED LATCH EC NO: S2012-0754 DRWN: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10	QUALITY SYMBOLS $F_A=0$ $F_G=0$ $F_P=0$	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE <b>MM ONLY</b>		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
			mm	INCH	DRAWN BY	DATE	TITLE			
		4 PLACES	± ---	± ---	JAKEEMEW	2009/04/22	DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR			
		3 PLACES	± ---	± ---	CCTEH	2009/05/27				
2 PLACES	± 0.20	± ---	APPROVED BY		DATE	MOLEX INCORPORATED				
1 PLACE	± ---	± ---	SHLENI		2010/12/15					
ANGULAR ± 3 °		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		MATERIAL NO. SEE TABLE		DOCUMENT NO. SD-78507-002		SHEET NO. 9 OF 10		
SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION								

PART NO.	VOLTAGE KEY POSITION	PLATING OPTION	LATCH COLOR	TRAY TYPE	PICK & PLACE CAP	PCB SECURE FUNCTION	PRODUCT SPEC	
78507-0011	CENTER (1.5V)	0.76µM (30µM) MIN. GOLD ON CONTACT. 2.54µM (100µM) MIN. PURE TIN ON SOLDER TAIL. 1.27µM (50µM) MIN. NICKEL UNDERPLATE.	BLACK	HARD TRAY (PK-78507-002)	YES	TYPE - E	PS-78507-001	
78507-0012						TYPE - B		
78507-0013						TYPE - C		
78507-0021						TYPE - E		
78507-0022						TYPE - B		
78507-0023					TYPE - C			
78507-0051					NATURAL (OFF-WHITE)	YES		TYPE - E
78507-0052								TYPE - B
78507-0053								TYPE - C
78507-0054								TYPE - D
78507-0061		TYPE - E						
78507-0062		TYPE - B						
78507-0063		TYPE - C						
78507-0102		0.38µM (15µM) MIN. GOLD ON CONTACT. 2.54µM (100µM) MIN. PURE TIN ON SOLDER TAIL. 1.27µM (50µM) MIN. NICKEL UNDERPLATE.	BLACK	HARD TRAY (PK-78507-002)	YES	TYPE - B		
78507-0103						TYPE - C		
78507-0112						TYPE - B		
78507-0113						TYPE - C		
78507-0142						TYPE - B		
78507-0143					TYPE - C			
78507-0152					TYPE - B			
78507-0153					TYPE - C			
78507-0202					NATURAL (OFF-WHITE)	NO		TYPE - B
78507-0203								TYPE - C
78507-0212		TYPE - B						
78507-0213		TYPE - C						
78507-0242	TYPE - C							
78507-0243	0.51µM (20µM) MIN. GOLD ON CONTACT. 2.54µM (100µM) MIN. PURE TIN ON SOLDER TAIL. 1.27µM (50µM) MIN. NICKEL UNDERPLATE.	BLACK	HARD TRAY (PK-78507-002)	YES	TYPE - B			
78507-0252					TYPE - C			
78507-0253					TYPE - B			
				NATURAL (OFF-WHITE)	NO	TYPE - C		
						TYPE - B		

<b>UPDATED LATCH</b> EC NO: S2012-0754 DRW: CCTEH 2012/04/17 CHKD: CGTAN 2012/05/10 APPR: SHLENI 2012/05/10 REV	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION
	$\nabla_A = 0$ $\nabla_C = 0$ $\nabla_P = 0$	mm INCH	MM ONLY	NTS	METRIC	
		4 PLACES ± --- ± --- 3 PLACES ± --- ± --- 2 PLACES ± 0.20 ± --- 1 PLACE ± --- ± --- ANGULAR ± 3 °	DRAWN BY DATE JAKEEMEW 2009/04/22 CHECKED BY DATE CCTEH 2009/05/27 APPROVED BY DATE SHLENI 2010/12/15	TITLE	DDR3 DIMM, 1.00 PITCH 240CKTS, LSP, STD LATCH SMT, VERTICAL, LOWLLCR	
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS	MATERIAL NO. SEE TABLE SIZE A3	DOCUMENT NO. MOLEX INCORPORATED SD-78507-002	SHEET NO. 10 OF 10	